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MG Klenzle, KC Sevcik - Proceedings of the 1979 ACM SIGMETRICS conference ..., 1979 - portal.acm.org

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R Olsen, M Laor, C Filsfil - US Patent App. 10/840,126, 2004 - Google Patents

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Based on purpose, manufacturing system models can be classified into two ...

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M Zukerman, G Shi, TH Theimer, EP Rathgeb, ... - Journal of the ACM, 1991 - cs.umanitoba.ca

... switch architectures. In this paper we will give a **classification** of the most commonBanyan networks and. ... ({CR}) **classification** scheme",. journal = "ACM comp. ...[View as HTML](#) - [All 2 versions](#)

A large-scale asynchronous transfer mode switch architecture

Y Shobatake, T Kamitake - Electronics and Communications in Japan (Part I: ...), 1996 - interscience.wiley.com

... Crosspoint buffer type Fig. 2. **Classification** of ATM switch architectures.

2.2.1. Efficient cell buffer usage in an element switch As is ...

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Comparison of multilevel methods for Kronecker-based Markovian representations

P Buchholz, T Dayar - Computing, 2004 - Springer

... HMMs with multiple macrostates. AMS Subject **Classifications**: 65F10 (primary),

60J27 (secondary). Keywords: Multilevel methods, multigrid ...

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Coordinated cell discharge from ATM queue

A Lundbck, LG Petersen, O Engstrm - US Patent 6,597,698, 2003 - Google Patents

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LG Petersen, A Lundbck, JM Agnevik - US Patent 6,504,845, 2003 - Google Patents

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